

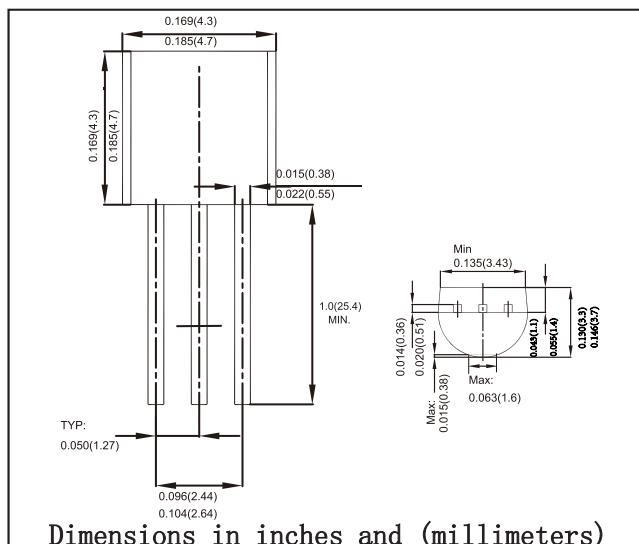
TO-92 Plastic-Encapsulate Transistors

FEATURES

- Switching and amplification in high voltage
- Applications such as telephony
- Low current
- High voltage
- NPN Transistors

MECHANICAL DATA

- Case style: TO-92 molded plastic
- Mounting position: any



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND CHARACTERISTICS

@ 25°C Ambient Temperature (unless otherwise noted)

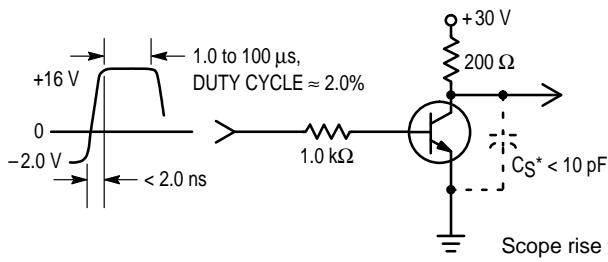
Parameter	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	0	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	3	V
Collector Current -Continuous	I_C	0.6	A
Collector Power Dissipation	P_D	625	mW
Thermal Resistance From Junction To Ambient	R_{KJA}	200	°C /W
Junction Temperature	T_j	100	°C
Storage Temperature	T_{stg}	-55~+150	°C

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C= 0.1\text{mA}, I_E=0$	60			V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C=1\text{mA}, I_B=0$	40			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E=0.1\text{mA}, I_C=0$	6			V
Collector cut-off current	I_{CBO}	$V_{CB}=60\text{V}, I_E=0$		0.1		mA
Emitter cut-off current	I_{EBO}	$V_{EB}=6\text{V}, I_C=0$		0.1		mA
DC current gain	H_{FE}	$V_{CE}=1\text{V}, I_C=1\text{mA}$	20			
		$V_{CE}=1\text{V}, I_C=10\text{mA}$	40			
		$V_{CE}=1\text{V}, I_C=150\text{mA}$	50		150	
		$V_{CE}=2\text{V}, I_C=500\text{mA}$	20			
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$I_C=150\text{mA}, I_B=15\text{mA}$		0.4		V
		$I_C=500\text{mA}, I_B=50\text{mA}$			0.75	
Base-emitter saturation voltage	$V_{BE(\text{sat})}$	$I_C=150\text{mA}, I_B=15\text{mA}$	0.75		0.95	
		$I_C=500\text{mA}, I_B=50\text{mA}$			1.2	V
Collector output capacitance	C_{ob}	$V_{CB}=5\text{V}, I_E=0, f=1\text{MHz}$			6.5	pF
Emitter input capacitance	C_{ib}	$V_{EB}=5\text{V}, I_C=0, f=1\text{MHz}$			30	pF
Transition frequency	f_T	$V_{CE}=10\text{V}, I_C=20\text{mA}, f=100\text{MHz}$	200			MHz

* Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2.0\%$.

RATINGS AND CHARACTERISTIC CURVES

SWITCHING TIME EQUIVALENT TEST CIRCUITS



Scope rise time < 4.0 ns

*Total shunt capacitance of test jig connectors, and oscilloscope

Figure 1. Turn-On Time

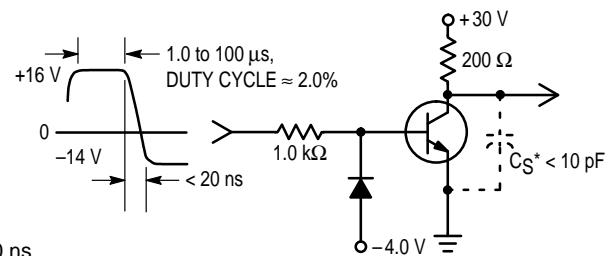


Figure 2. Turn-Off Time

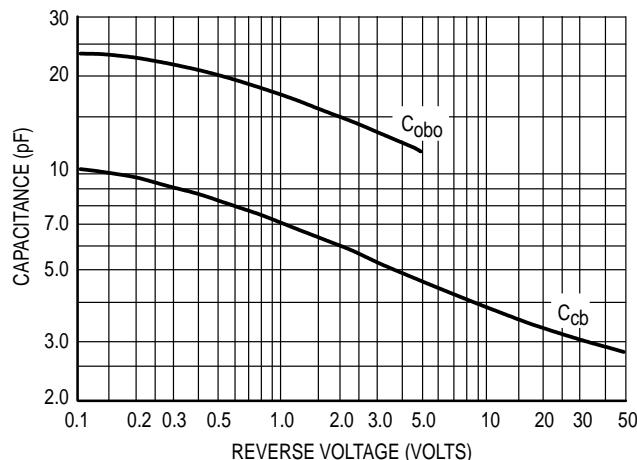


Figure 3. Capacitances

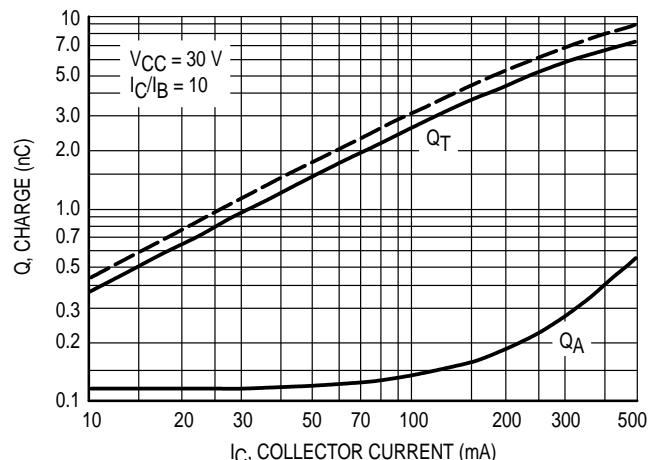


Figure 4. Charge Data

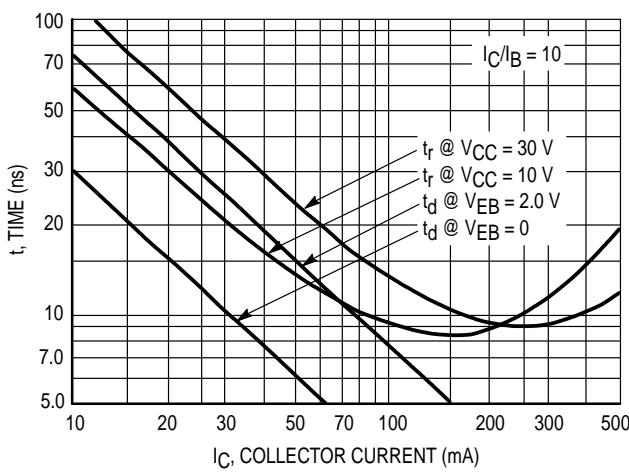


Figure 5. Turn-On Time

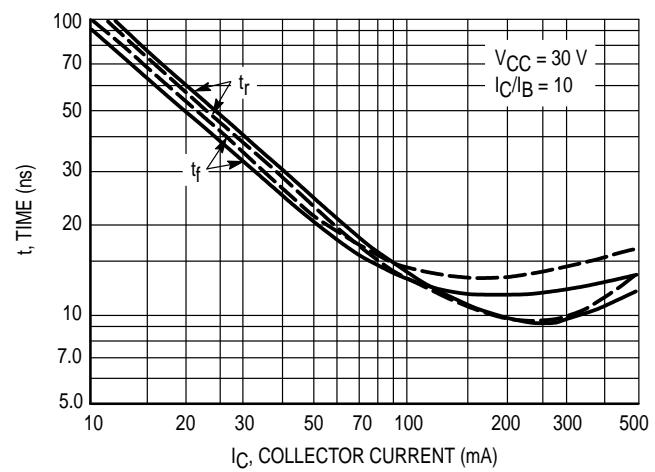


Figure 6. Rise and Fall Times

RATINGS AND CHARACTERISTIC CURVES

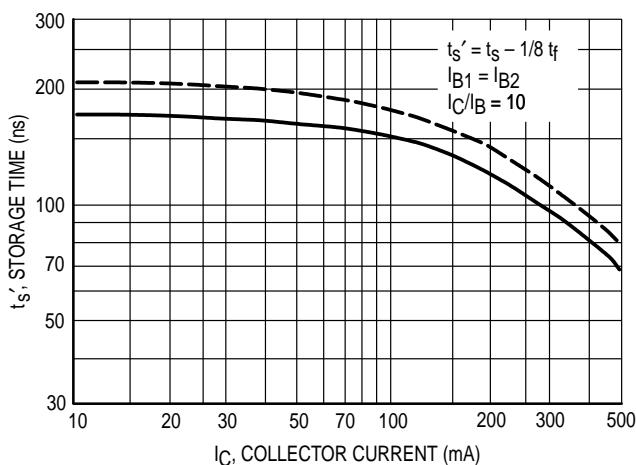


Figure 7. Storage Time

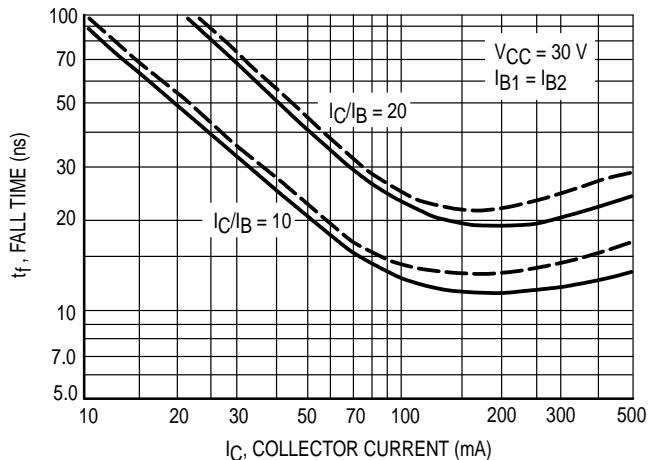


Figure 8. Fall Time

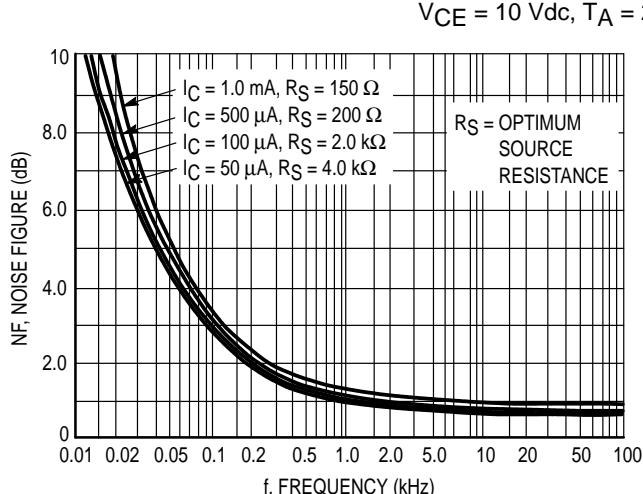


Figure 9. Frequency Effects

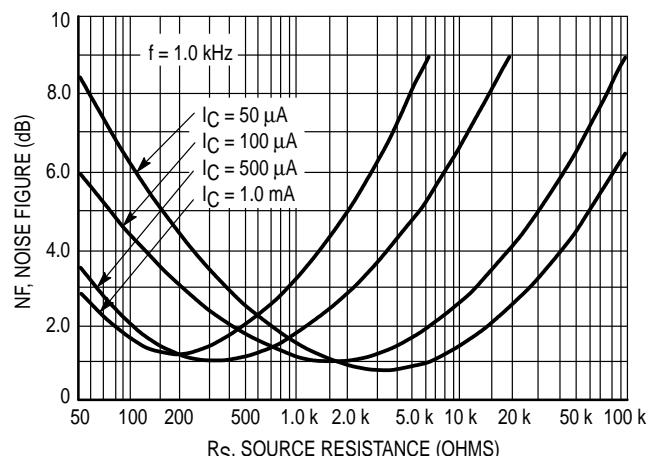


Figure 10. Source Resistance Effects

h PARAMETERS

$V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz}, T_A = 25^\circ\text{C}$

This group of graphs illustrates the relationship between h_{FE} and other "h" parameters for this series of transistors. To obtain these curves, a high-gain and a low-gain unit were selected from both the 2N4400 and 2N4401 lines, and the same units were used to develop the correspondingly numbered curves on each graph.

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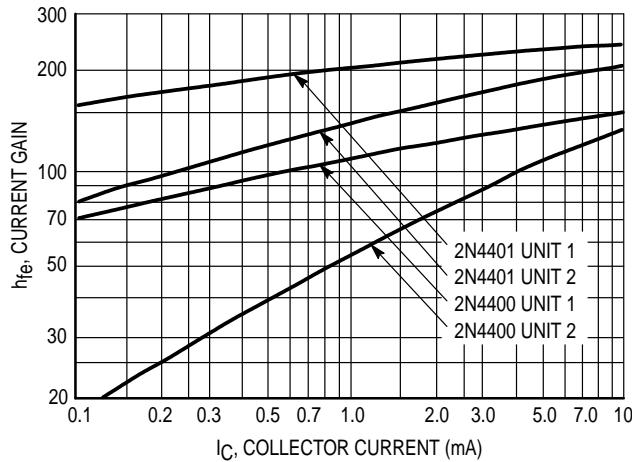


Figure 11. Current Gain

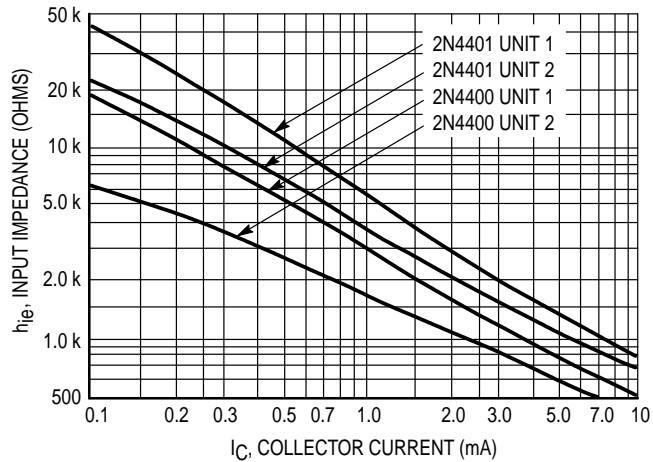


Figure 12. Input Impedance

RATINGS AND CHARACTERISTIC CURVES

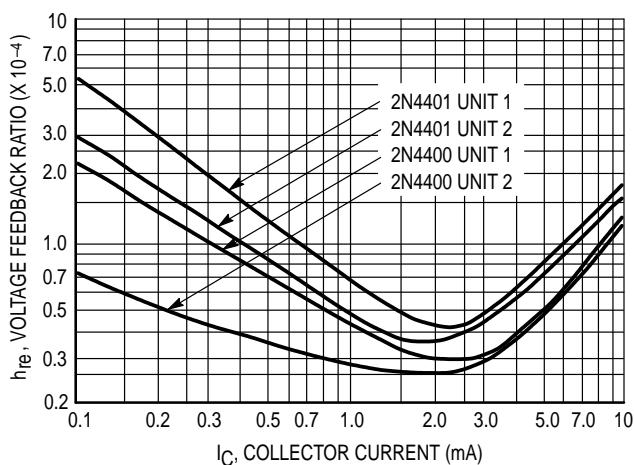


Figure 13. Voltage Feedback Ratio

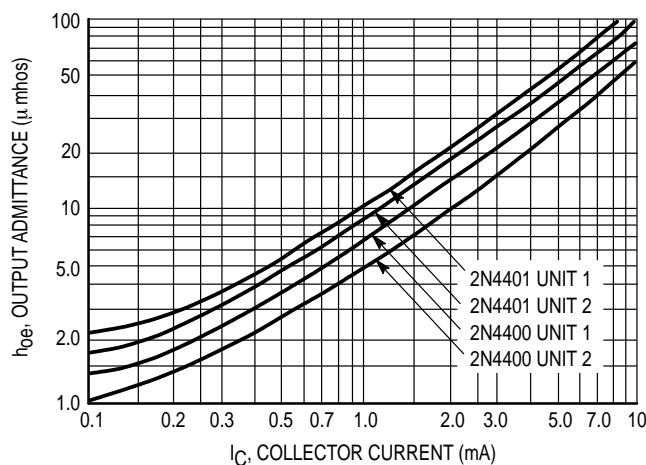


Figure 14. Output Admittance

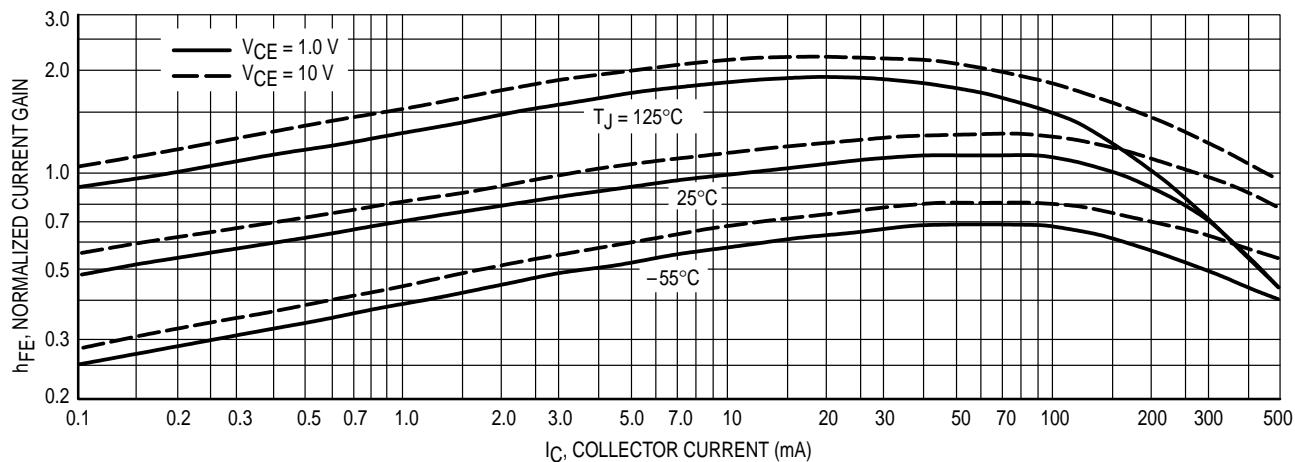


Figure 15. DC Current Gain

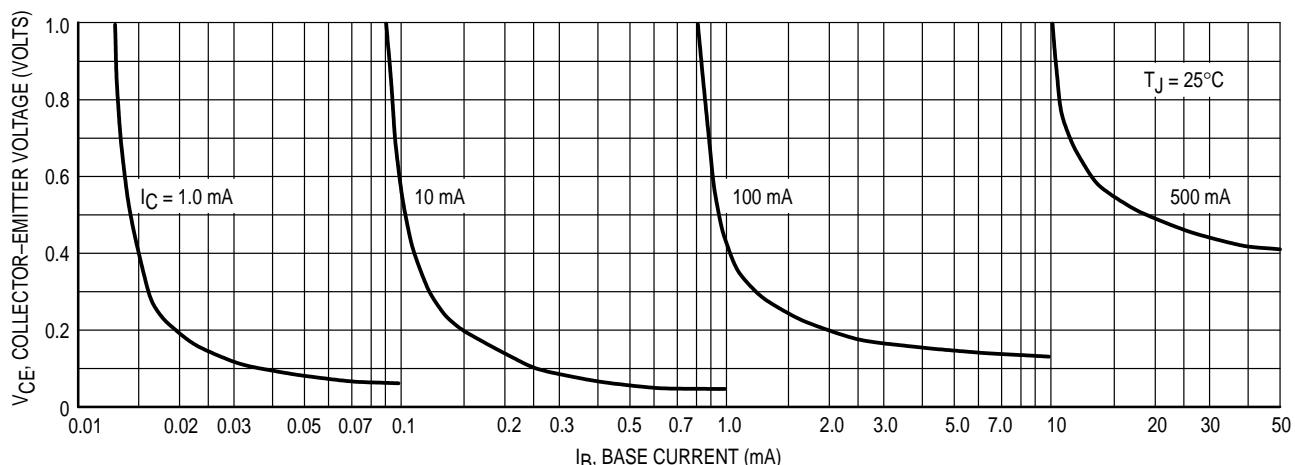


Figure 16. Collector Saturation Region

RATINGS AND CHARACTERISTIC CURVES

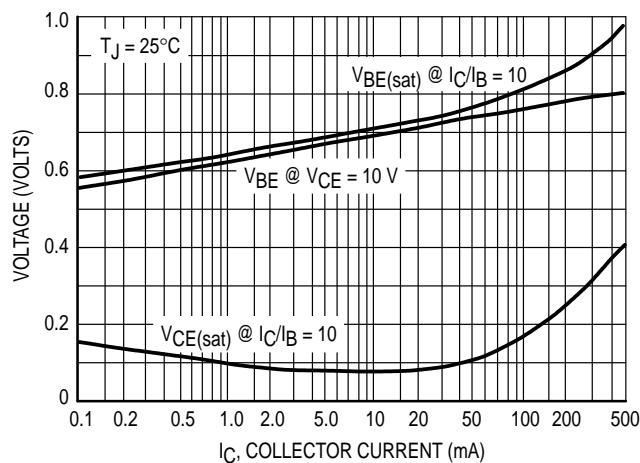


Figure 17. "On" Voltages

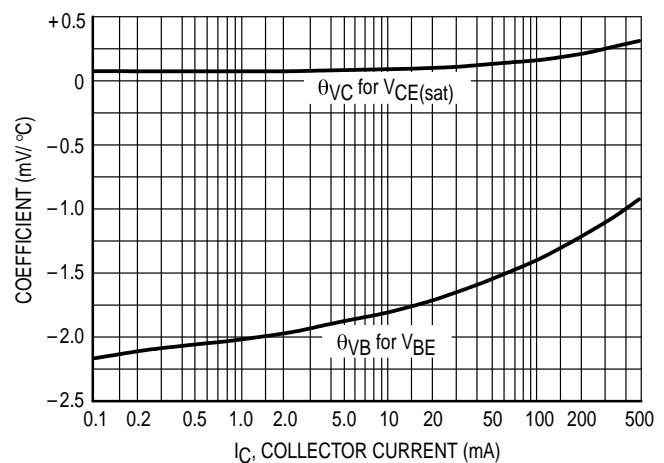


Figure 18. Temperature Coefficients